

Application Data Sheet**Application Information**

Application Type::	Regular
Subject Matter::	Utility
CD-ROM or CD-R:	None
Title::	SEMICONDUCTOR INTEGRATED CIRCUIT, ELECTRONIC EQUIPMENT, AND TRANSISTOR BACK-GATE VOLTAGE CONTROL METHOD
Attorney Docket Number::	119408
Total Drawing Sheets::	8
Small Entity::	No

Applicant Information

Applicant Authority type::	Inventor
Primary Citizenship Country::	Japan
Status::	Full Capacity
Given Name::	Nobuo
Family Name::	KARAKI
City of Residence::	Suwa-shi
Country of Residence::	Japan

Correspondence Information

Correspondence Customer Number::	25944
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Foreign Priority Information			
Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	2003-112088	04/16/2003	Yes
Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	2004-044584	02/20/2004	Yes
Assignee Information			
Assignee Name::		Seiko Epson Corporation	
Street of mailing address::		4-1, Nishishinjuku 2-chome, Shinjuku-ku	
City of mailing address::		Tokyo	
Country of mailing address::		Japan	
Postal or Zip Code of mailing address::		163-0811	